

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1088	(oled or oeld or led) and 345/76,77,82-84,204.ccls.	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:10
L2	4536	((current adj (driven or driving) adj element) or oled or oeld or led) same transistor same between	US-PGPU B; USPAT	OR	ON	2005/02/25 17:24
L3	163	2 and 345/76,77,82-84, 204.ccls.	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:24
L4	56	l3 and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:25
L5	420	345/76.ccls. and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:19
L6	54	l5 and (n-channel same p-channel)	US-PGPU B; USPAT	OR	ON	2005/02/25 17:20
L7	872	345/204.ccls. and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:21
L8	75	l7 and (n-channel same p-channel)	US-PGPU B; USPAT	OR	ON	2005/02/25 17:22
L9	1361	315/169.1,169.3.ccls. and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:22
L10	84	l9 and (n-channel same p-channel)	US-PGPU B; USPAT	OR	ON	2005/02/25 17:22
L11	5361	((current adj (driven or driving) adj element) or oled or oeld or led or el) same transistor same between	US-PGPU B; USPAT	OR	ON	2005/02/25 17:35
L12	322	11 and 345/76,77,82-84, 204.ccls.	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:27
L13	107	l12 and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:27
L14	51	l13 not l4	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:25
L15	0	11 and "315"".""169". 1-169.3.ccls.	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:27

L16	217	11 and 315/169.1-169.3. ccls.	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:27
L17	107	l16 and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:39
L18	2	"3443151".uref.	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:31
L19	0	tam-s\$,in,	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	OFF	2005/02/25 17:32
L20	371	tam-s\$.in.	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	OFF	2005/02/25 17:32
L21	10	l20 and oeld	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	OFF	2005/02/25 17:32
L22	1187	((current adj (driven or driving) adj element) or oled or oeld or led or el) same transistor same between	EPO; JPO; DERWEN T	OR	ON	2005/02/25 17:35
L23	10	l22 and p-channel and n-channel	EPO; JPO; DERWEN T	OR	ON	2005/02/25 17:37
L24	0	"345"/\$.ccls and (p-channel same n-channel same oled or oeld or el)	US-PGPU B; USPAT	OR	ON	2005/02/25 17:38

L25	0	"345"/\$.ccls and (p-channel same n-channel same (oled or oeld or el or current adj driven adj element))	US-PGPU B; USPAT	OR	ON	2005/02/25 17:39
L26	209	"345"/\$.ccls. and (p-channel same n-channel same (oled or oeld or el or current adj driven adj element))	US-PGPU B; USPAT	OR	ON	2005/02/25 17:39
L27	74	l26 and @ad<="20010709"	US-PGPU B; USPAT	OR	OFF	2005/02/25 17:39